



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Leonard Forbes

Serial No.: 09/943,968

Filed: August 30, 2001

For: TECHNIQUE TO
SIMULTANEOUSLY DISTRIBUTE
CLOCK SIGNALS AND DATA ON
INTEGRATED CIRCUITS, INTERPOSERS,
AND CIRCUIT BOARDS

Confirmation No.: 1619

Examiner: D. Ha

Group Art Unit: 2634

Attorney Docket No.: 2269-4720US
(00-1120.00/US)

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EL994845975US

Date of Deposit with USPS: September 6, 2005

Person making Deposit: Steve Wong

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the official filing receipt received from the PTO in the above application.

The error to be corrected is as follows:

In the title, after "Technique to" change "simultaneously" to --simultaneously--.

Serial No. 09/943,968

Issuance of a corrected filing receipt in accordance with the annotations on the attached filing receipt copy is respectfully requested.

Respectfully submitted,



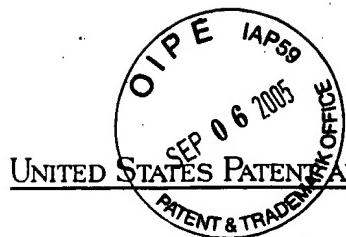
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Date: September 6, 2005

JMM/dlm

Enclosure: Copy of Official Filing Receipt with annotations correcting errors

Document in ProLaw



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
 UNITED STATES PATENT AND TRADEMARK OFFICE
 WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/943,968	08/30/2001	2631	1294	4720US (00 - 1120)	6	48	4

24247
 TRASK BRITT
 P.O. BOX 2550
 SALT LAKE CITY, UT 84110

RECEIVED

OCT 11 2001

Trask Britt

CONFIRMATION NO. 1619

FILING RECEIPT



OC000000006846945

Date Mailed: 10/04/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Leonard Forbes, Corvallis, OR;

Domestic Priority data as claimed by applicant**Foreign Applications**

If Required, Foreign Filing License Granted 10/03/2001

Projected Publication Date: 03/06/2003

Non-Publication Request: No

Early Publication Request: No

Title**simultaneously**

Technique to simultaneously distribute clock signals and data on integrated circuits, interposers, and circuit boards

Preliminary Class

375